

B

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. RA001C10)

In the Application of:)

FARMWALD ET AL.)

Serial No: CONTINUATION OF 09/252,998)

Filed: FEBRUARY 28, 2000)

Title: INTEGRATED CIRCUIT I/O USING A
HIGH PERFORMANCE BUS INTERFACE)

#3 / Pro Aeneas B
J. J. Dayno
G/O
Group
Art Unit:

Before
Examiner:

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-referenced application,
kindly amend the application as follows:

IN THE ABSTRACT:

Please delete the Abstract of the Disclosure and substitute the
attached Abstract of the Disclosure.

IN THE TITLE:

Please delete the title and substitute --METHOD AND APPARATUS FOR
CONTROLLING A SYNCHRONOUS MEMORY DEVICE--.

B